

---

--Abstract of the Disclosure

A1  
To reduce intersymbol interference, a system transmitter has an outer encoder, an interleaver for the encoded information, a recursive inner coder, and a modulator coupled to a channel. A receiver has a channel characteristics estimator coupled to the channel, a converter using a SISO equalizer, a deinterleaver and a SISO decoder.--

---

Please delete the paragraph starting on page 8, line 11 through line 19 and replace with the following replacement paragraph:

---

A2  
In the system of Fig. 2, the first FSM in the transmitter in the outer encoder 202, designated also as FSM1. The second FSM or FSM2 consists of the recursive inner encoder block 204 and the memoryless modulator block 205. The third FSM or FSM3 is the ISI channel representation 206 and the fourth FSM or FSM4 is a combination of the second FSM and the third FSM. In the receiver the SISO decoder 211 tries to track down the state transitions that have taken place in the outer encoder 202. For doing this it uses its preprogrammed knowledge about the allowed transitions; the preprogrammed knowledge appears in Fig. 2 as the oval block labeled (FSM1). Similarly the SISO equalizer 209 retrieves the operation of the entity formed by the memory block 204 and the memoryless modulator 205, by using the preprogrammed knowledge represented by (FSM2).

---

IN THE CLAIMS:

Please replace the following claim(s) as rewritten below: